

Gate Driver Unit 2LG08xFxC11M

■ Overview

2LG08xFxC11M is a dual channel gate driver designed for Fuji Electric's IGBT power module 2MBI1200XZF230-50 .

This gate driver unit contains a built-in isolated DC/DC converter and gate drive circuit.

It is ready to use by mounting it on the IGBT power module.

■ Features

- Ideal for drive of IGBT Power module 2MBI1200XZF230-50(Fuji Electric)
- Gate voltage : +15V/-10V
- Gate resistor : +0.22Ω/-1.8Ω(TYP)
- Short circuit detection voltage : 50V(TYP)
- ALL-IN-ONE (Built-in isolated DC / DC converter and gate drive circuit)
- Low parasitic capacitance (18pF(TYP)) ; highly resistant to common-mode noise.
- Fast response : about 140ns(TYP)
- The isolation for primary-secondary signal used fast response isolator.
- Dielectric withstand voltage : AC5000V
- Insulation distance (clearance / creepage) : 14mm (As for Gate driver module PCB)
- DC/DC converter input voltage : 13~28V
- Power supply for gate driver input voltage : 13~28V
- Signal input voltage : 3.3~15V or 15V
- The DC / DC converter has built-in overheat protection and overload protection.
- Desaturation protection (Gate drive circuit)
- Soft turn-off function (Gate drive circuit)
- Fault signal output function (Gate drive circuit)
- Under-voltage lockout(UVLO) (Gate drive circuit)
- Direct mode / Half bridge mode can be switched. (Gate drive circuit)
- Parallel drive possible by connected a follower unit
- PWM output function for NTC thermistor temperature inside the device (Option)
- Safety standards : UL508(file no.E243511) (DC/DC converter only)
- Reinforced isolation according to IEC 60664-1 (IEC61800-5-1, IEC62477-1, IEC62109-1, etc.)
- UL compliant (UL1741, UL508, etc.)
- Insulating moistureproof coating

■ Application

Industrial inverter, power conditioner, etc. ...

■ Module Information

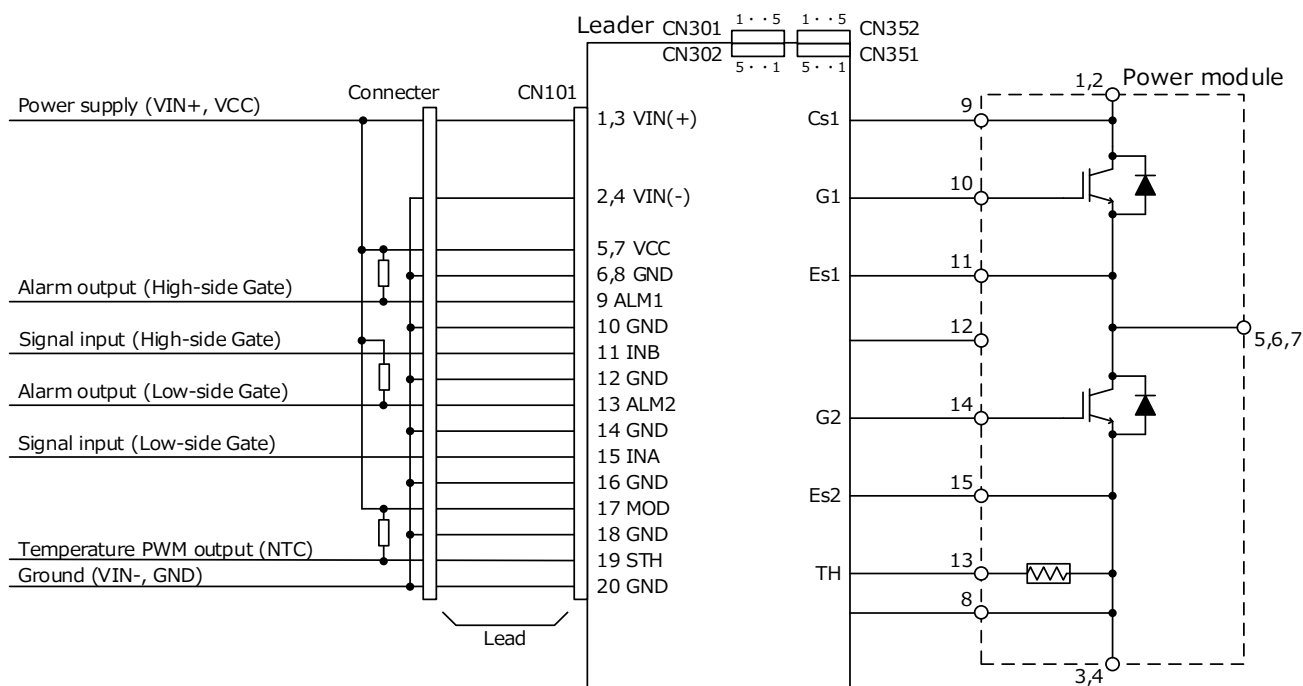
Part number	Part number (Uncoated)	Signal input voltage	Active clamp	TH PWM output	Status
2LG08ACCC11M	2LG08ACCN11M	3.3~15V	None	Yes	Planing
2LG08ACDC11M	2LG08ACDN11M	15V	None	Yes	In development

*Uncoated type is sample only

*Refer to the [2LG08xGZCZ1S] data sheet for follower units.

This target specification is subject to be changed without notice.

■ Circuit Image (No thermistor isolated amplifier output function)



■ Pin Connection

CN101 : RA-H201TD (JST)

Pin No.	Name	Function	Pin No.	Name	Function
1	VIN(+)	Power supply for DC/DC converter(+)	2	VIN(-)	Power supply for DC/DC converter(-)
3	VIN(+)	Power supply for DC/DC converter(+)	4	VIN(-)	Power supply for DC/DC converter(-)
5	VCC	Power supply for signal control circuit	6	GND	Ground for drive circuit
7	VCC	Power supply for signal control circuit	8	GND	Ground for drive circuit
9	ALM1	Alarm signal output 1 (High side)	10	GND	Ground for drive circuit
11	INB	Control input B (High side)	12	GND	Ground for drive circuit
13	ALM2	Alarm signal output 2 (Low side)	14	GND	Ground for drive circuit
15	INA	Control input A (Low side)	16	GND	Ground for drive circuit
17	MOD	Mode select	18	GND	Ground for drive circuit
19	STH	PWM signal output for NTC thermistor measurement	20	GND	Ground for drive circuit

※Reference receptacle : RA-S201T (JST)

Connection on the power module

No.	Name	CH	Function	No.	Name	CH	Function
9	Cs1	1(H)	Collector connection, High side	14	G2	2(L)	Gate connection, Low side
10	G1	1(H)	Gate connection, High side	15	Es2	2(L)	Emitter connection, Low side
11	Es1	1(H)	Emitter connection, High side	13	TH	2(L)	Thermistor connection, Low side
12	None	-	Electrical connection is not allowed	8	None	-	Electrical connection is not allowed

■ Pin Connection for Parallel

CN301 : B05B-PASK(LF)(SN) (JST)

Pin No.	Name	CH	Function
1	CC2A1	2	Connect the communication line for parallel drive 2LG08xGZCZ1S series
2	CC2A2	2	
3	CC2A3	2	
4	CC2A4	2	
5	CC2A5	2	

※Reference receptacle : PAP-05V-S (JST)

CN302 : B05B-PASK(LF)(SN) (JST)

Pin No.	Name	CH	Function
1	CC2B5	2	Connect the communication line for parallel drive 2LG08xGZCZ1S series
2	CC2B4	2	
3	CC2B3	2	
4	CC2B2	2	
5	CC2B1	2	

※Reference receptacle : PAP-05V-S (JST)

CN352 : B05B-PASK(LF)(SN) (JST)

Pin No.	Name	CH	Function
1	CC1A5	1	Connect the communication line for parallel drive 2LG08xGZCZ1S series
2	CC1A4	1	
3	CC1A3	1	
4	CC1A2	1	
5	CC1A1	1	

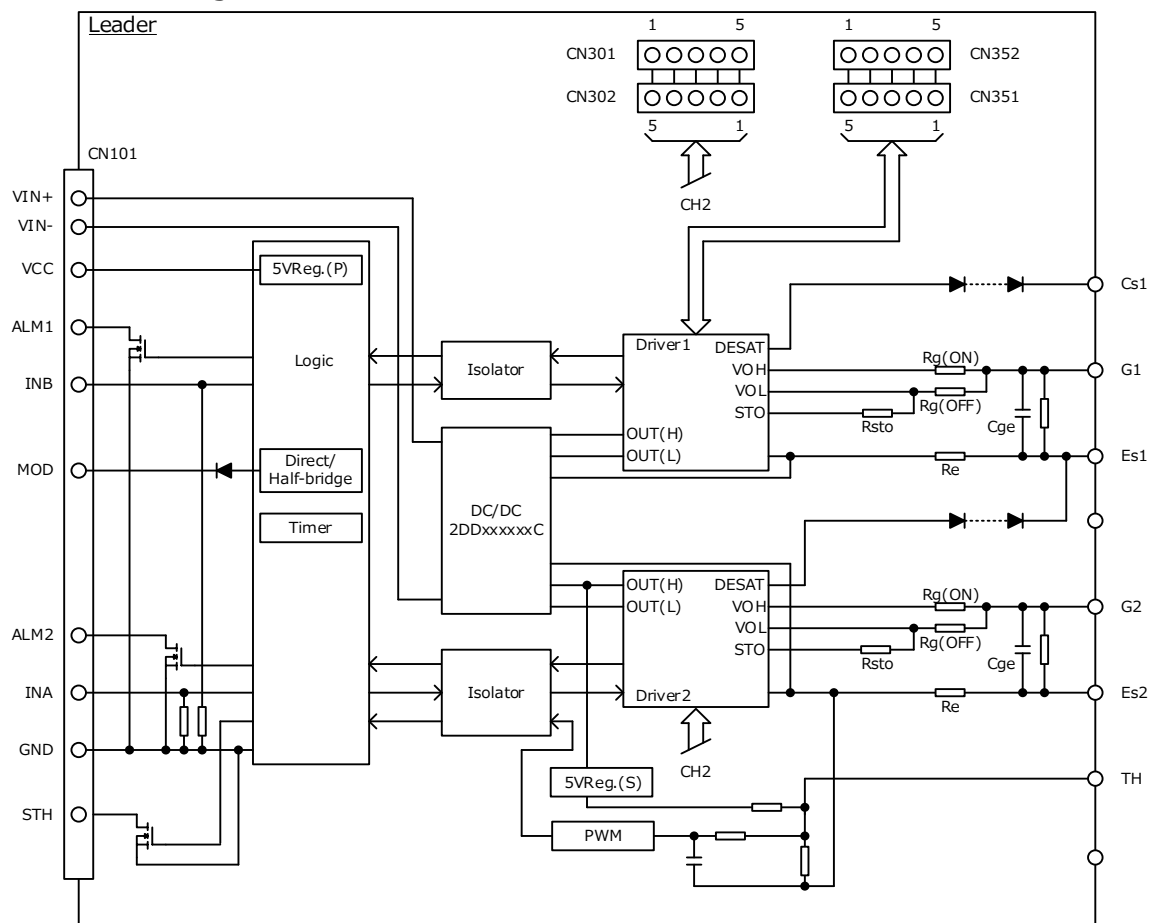
※Reference receptacle : PAP-05V-S (JST)

CN351 : B05B-PASK(LF)(SN) (JST)

Pin No.	Name	CH	Function
1	CC1B1	1	Connect the communication line for parallel drive 2LG08xGZCZ1S series
2	CC1B2	1	
3	CC1B3	1	
4	CC1B4	1	
5	CC1B5	1	

※Reference receptacle : PAP-05V-S (JST)

■ Internal Block Diagram



■ Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Conditions・Note
Input voltage for DC/DC converter	V_{IN}	-0.3	28	Vdc	Between VIN(+) to VIN(-)
Input voltage for Gate driver	V_{CC}	-0.3	28	Vdc	Between VCC to GND
Input-side signal voltage	V_{SG}	-0.3	$V_{CC}+0.3$ or 18 *	V	INA, INB *Whichever is less
	V_{MOD}	-0.3	28	V	MOD
	V_{RTC}	-0.3	5	V	RTC
Maximum gate current	I_{GPEAK}	-	43	A	Excluding gate resistor
Switching frequency	F_{SW}	-	12	kHz	See the permissible frequency curve
Short circuit detection pin voltage	V_{SD}	0	2300	V	
Alarm signal output pin maximum voltage	V_{ALM}	-	$V_{CC}+0.3$ or 28 *	V	ALM1,2 *Whichever is less
Input-side signal maximum current	I_{ALM}	-	5	mA	ALM1,2
Operating temperature range	$V_{IN}=13.5-18V$ T_{OP}	-40	85	°C	See the permissible frequency curve
	$V_{IN}=18-26.4V$ T_{OP}	-40	75	°C	
Operating humidity	RH_{OP}	20	95	%RH	No condensation
Storage temperature range	T_{STG}	-40	90	°C	
Storage humidity	RH_{STG}	5	95	%RH	No condensation
PWM output function for NTC thermistor					
PWM signal output pin maximum voltage	V_{STHO}	-	$V_{CC}+0.3$ or 28 *	V	*Whichever is less

■ Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions・Note
Input voltage range for DC/DC converter	V_{IN}	13.5	26.4	Vdc	
Input voltage range for gate driver	V_{CC}	13.5	26.4	Vdc	
Driver circuit number	N	-	2	-	
Maximum gate charge	Q_G	-	11000	nC	*1
Switching frequency	IGBT n : 1 F_{SW}	-	8.8	kHz	Test load : 1.67Ω/440nF *2
	IGBT n : 2 F_{SW}	-	5	kHz	
MOD pin high input voltage	V_{MODH}	3.3	26.4	V	
MOD pin low input voltage	V_{MODL}	-0.3	0.5	V	
2LGxxxxDx11M					
Logic high level input voltage	V_{SGH}	13	$V_{CC}+0.3$ or 16 *	V	INA, INB *Whichever is less
Logic low level input voltage	V_{SGL}	-0.3	0.5	V	INA, INB
Source current of control signal	I_{SG}	3.3	-	mA	INA, INB $V_{SG}=15V$
2LGxxxxCx11M					
Logic high level input voltage	V_{SGH}	3.3	$V_{CC}+0.3$ or 16 *	V	INA, INB *Whichever is less
Logic low level input voltage	V_{SGL}	-0.3	0.5	V	INA, INB
Source current of control signal	I_{SG}	3.2	-	mA	INA, INB $V_{SG}=15V$

*1 If the gate charge exceeds the allowable value, the gate voltage at turn-on and turn-off will drop, which may affect the switching performance of the IGBT.

If you are considering using it under conditions other than the recommended conditions, please contact us.

*2 Use below the recommended Conditions.

If you are considering using it under conditions other than the recommended conditions, please contact us.

■ I/O Condition Table

No.	Status	Input						Output				Note
		OUT(H)	Cs1(H)	Es1(L)	MOD	INB	INA	ALM1	ALM2	G1(H)	G2(L)	
1	V _{OUT} UVLO	UVLO	X	X	X	X	X	L	L	L	L	Output state in UVLO (V _{out} < UVLO _{th})
2	G-E short	○	X	X	X	X	X	L	L	SD	SD	Output state in Gate-Emitter short
3	Normal operation	○	-	L	H	-	L	-	Hi-Z	-	L	Low side I/O state in direct mode (MOD pin H)
4		○	-	L	H	-	H	-	Hi-Z	-	H	
5		○	L	-	H	L	-	Hi-Z	-	L	-	High side I/O state in direct mode (MOD pin H)
6		○	L	-	H	H	-	Hi-Z	-	H	-	
7		○	L	L	L	L	X	Hi-Z	Hi-Z	L	L	I/O state in half-bridge mode (MOD pin L)
8		○	L	L	L	H	L	Hi-Z	Hi-Z	H	L	
9		○	L	L	L	H	H	Hi-Z	Hi-Z	L	H	
10	Short circuit detection (L)	○	-	>V _{SD}	H	-	L	-	Hi-Z	-	L	I/O state during low side DESAT protection in direct mode
11		○	-	>V _{SD}	H	-	H	-	L	-	L	I/O state during low side DESAT protection in half-bridge mode
12	Short circuit detection (H)	○	-	>V _{SD}	L	H	L	-	Hi-Z	-	L	I/O state during high side DESAT protection in direct mode
13		○	-	>V _{SD}	L	H	H	-	L	-	L	
14		○	>V _{SD}	-	H	L	-	Hi-Z	-	L	-	I/O state during high side DESAT protection in half-bridge mode
15		○	>V _{SD}	-	H	H	-	L	-	L	-	
16		○	>V _{SD}	-	L	H	H	Hi-Z	-	L	-	I/O state during high side DESAT protection in half-bridge mode
17		○	>V _{SD}	-	L	H	L	L	-	L	-	

G-E short : Gate-Emitter short

X : Don't care

- : Specified in other items

○ : OUT(H) > UVLO, X : Don't care

SD : Shut down (Gate-Emitter short)

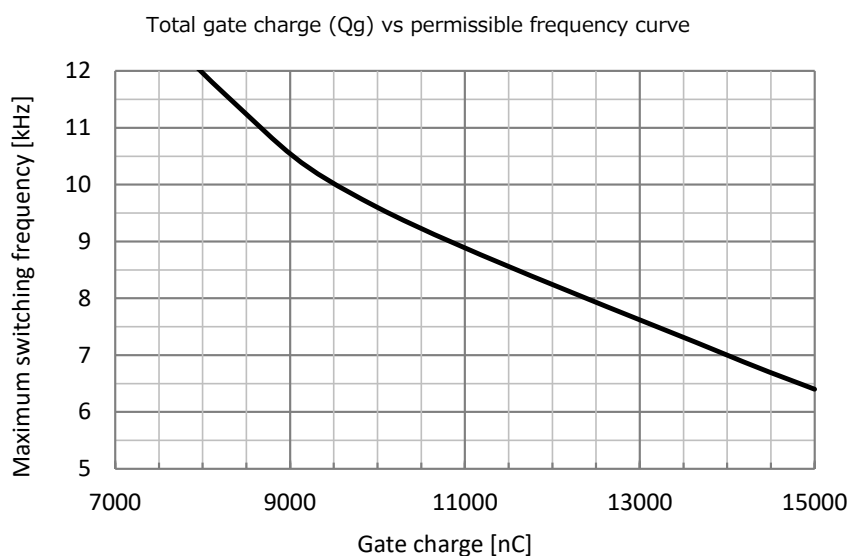
V_{SD} : Short circuit detection voltage

■ Permissible frequency curve

Internal gate resistor of IGBT = 1.67Ω

*The permissible frequency curve changes with the ratio of the IGBT internal gate resistance to the gate resistance.

Therefore, as the internal resistance of the IGBT decreases, the allowable frequency also decreases.


R_g: +0.22ohm/-1.8ohm

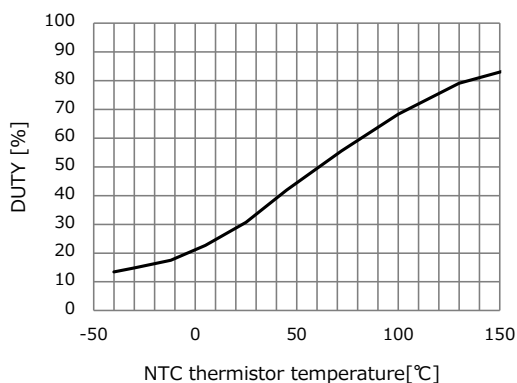
T_a: -40~+85°C / V_{IN} = 13.5~18V

T_a: -40~+75°C / V_{IN} = 18~26.4V

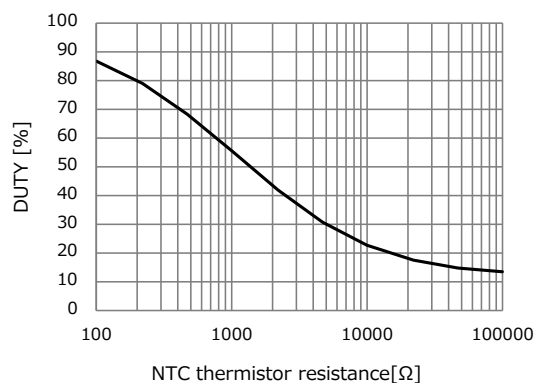
■ Electrical Specification (Vin=Vcc=15V, Ta=25°C, Unless otherwise specified)

Item	Symbol	Min	Typ	Max	Unit	Conditions · Note	
DC/DC converter							
Start-up voltage		V _{START}	-	11.5	12.5	V	
Input current	IGBT n : 1	I _{IN}	-	0.5	-	A	Fsw=8.8kHz,Test load : 1.67Ω/440nF
	IGBT n : 2		-	0.5	-		Fsw=5.0kHz,Test load : 1.67Ω/440nF
Standby power	IGBT n : 1	P _{STBY}	-	1.6	-	W	No load
	IGBT n : 2		-	1.8	-	W	No load
Logic 2LGxxxxDx11M							
Turn-on threshold		V _{SGH}	-	10.0	11.7	V	INA, INB / Guaranteed by design
Turn-off threshold		V _{SGL}	3.9	5.0	-	V	INA, INB / Guaranteed by design
Logic pull-down resistance		R _{SGD}	-	4500	-	Ω	INA, INB
Logic 2LGxxxxCx11M							
Turn-on threshold		V _{SGH}	-	2.0	2.4	V	INA, INB / Guaranteed by design
Turn-off threshold		V _{SGL}	0.8	1.0	-	V	INA, INB / Guaranteed by design
Logic pull-down resistance		R _{SGD}	-	4700	-	Ω	INA, INB
Gate driver output							
Output pin voltage(High)		V _{OUTH}	14	15	16	V	No load
Output pin voltage(Low)		V _{OUTL}	-11	-10	-9	V	No load
Gate resistor	Rg(ON)	-	0.22	-	Ω		
	Rg(OFF)	-	1.8	-			
Auxiliary gate capacitor		Cge	-	OPEN	-	nF	
Emitter resistor		Re	-	0.1	-	Ω	
Delay time	Turn ON time	t _{PON}	-	140	-	ns	
	Turn OFF time	t _{POFF}	-	140	-	ns	
Dead time		t _{DEAD}	-	3	-	us	Half bridge mode
PWM output function for NTC thermistor							
Carrier frequency		-	-	10	-	kHz	
Duty cycle	TH = 5000Ω	-	-	30.0	-	%	
	TH = 300Ω	-	-	75.0	-	%	

*NTC condition: R25 = 5kΩ, B(25/50) = 3375K

■ Thermistor Signal Output Curve


Duty cycle vs NTC temperature



Duty cycle vs NTC resistance

*NTC condition: R25 = 5kΩ, B(25/50) = 3375K

■ Protection

Item	Symbol	Min	Typ	Max	Unit	Conditions · Note
DC/DC converter						
Overload protection	-	10.5	-	-	W	Auto recovery
Overheat protection	-	120	-	150	°C	Auto recovery, Internal temperature
Gate driver						
Output voltage(H) UVLO OFF voltage	V_{UVLOOH}	13.2	13.5	13.8	V	Guaranteed by design
Output voltage(H) UVLO ON voltage	V_{UVLOOH}	12.2	12.5	12.8	V	Guaranteed by design
Short circuit detection voltage	V_{SD}	-	50	-	V	
Short circuit detection filter time	DC-link voltage=1000V	$t_{SHORTFIL}$	-	5.9	us	10% to 90% of VGS Target spec
	DC-link voltage=1200V	$t_{SHORTFIL}$	-	5.2	us	
	DC-link voltage=1500V	$t_{SHORTFIL}$	-	4.4	us	
Alarm signal output L voltage	V_{ALML}	-	-	0.5	V	$I_{ALM}=5mA$
Alarm signal output time	t_{ALM}	-	0.2	-	us	
Restart time	$t_{RESTART}$	-	110	-	ms	
Soft turn-off resistance	R_{STO}	-	10	-	Ω	
Soft turn-off duration	t_{STO}	-	6	-	us	

■ Insulation

Item	Specification	Conditions · Note
Between Input-Output		
Dielectric withstand voltage	AC5000V	1min, Cutoff 2mA
Insulation resistance	100MΩ or more	DC500V
Partial discharge extinction voltage	2760Vpeak or more	According to EN50178/IEC 60270
Common-mode transient immunity (CMTI)	70kV/us	
Minimum clearance distances	14mm	PCB:CTI PLC0, Case: CTI PLC0
Minimum creepage distances	14mm	
Between CH1-CH2		
Minimum clearance distances	8mm	Excluding electrical connections point
Minimum creepage distances	12mm	PCB:CTI PLC0, Case: CTI PLC0

■ Storage Conditions

Item	Min	Max	Unit	Conditions · Note
Storage temperature	-25	60	°C	A packing state

■ Usage Cautions

- Always mount fuse on the plus side of input for ensuring safety because the fuse is not built-in the product.
Please select the fuse considering conditions such as steady current, inrush current, and ambient temperature.
When using a fuse having large rated current or high capacity input electrolytic condenser, by combining another converter and input line and input electrolytic condenser, fuse may not blow off in the case of abnormality.
Do not combine high voltage line and fuse.

- Make sure the rise/fall time of the input signal is 500ns or less.
Also, keep input wiring as far as possible from noise sources.
To prevent malfunction due to noise, a high signal voltage within the recommended range is recommended.

- Please do not apply excessive stress to this product when attaching to IGBT power module.
Please follow the device manufacturer's instructions on how to install the IGBT power module (type of screw used, material, tightening torque conditions, etc.).
Also, The screw header / washer diameter uses the following.

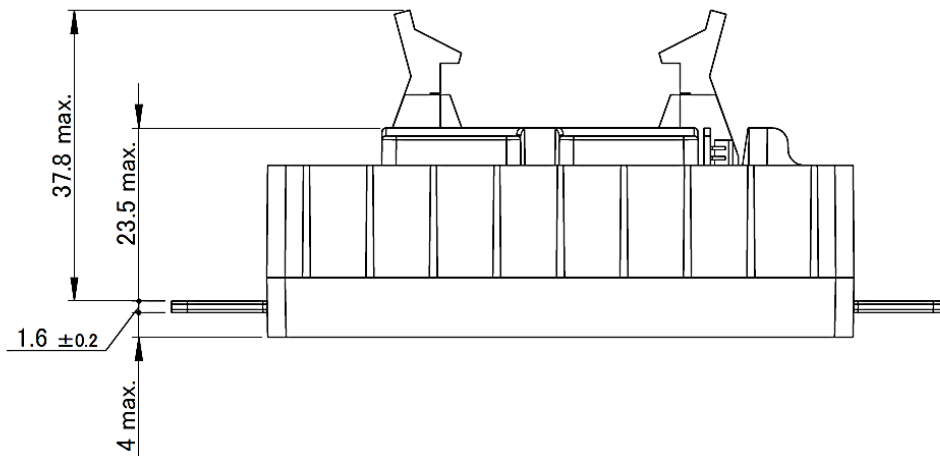
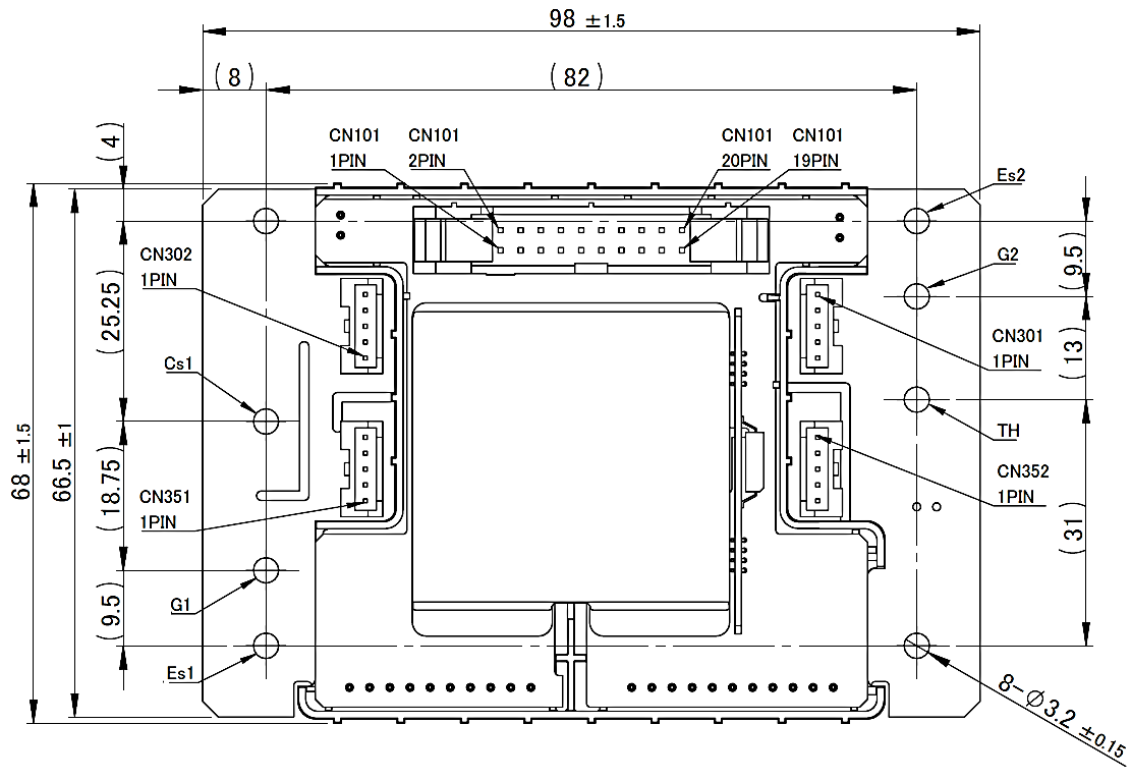
M3: 6mm or less	*To maintain the reliability of parts near the metal terminal pad, the screw header including the washer must not exceed the available metal terminal pad of the gate driver.
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- This product has DESAT protection for arm short circuit and load short circuit protection.
However, even if this protection works, the IGBT may be damaged if abnormally high current occurs due to IGBT's characteristics variations or the load short-circuit mode during parallel operation.
To ensure safety, be sure to check the short-circuit current at the unit in which this product is integrated, and evaluate whether it can protect under the condition that there is no damage to the IGBT.

- The coating material is applied to the product, so it may appear to be partially whitened.
This does not affect the characteristics of the product.

With Thermistor insulation circuit

■ Outline Dimensional Drawing



Unit: mm

Note: 1. The dimensional tolerance without directions is ± 0.5 mm.

■ Product Weight

96.0g(typ)

■ Important Notice

- This information and product are subject to change without prior notice for the purpose of improvements, etc.
Ensure that you are in possession of the most up-to-date information when using this product.
- The operation examples and circuit examples shown in this document are for reference purposes only, and TAMURA Corporation disclaims all responsibility for any violations of industrial property rights, intellectual property rights and any other rights owned by TAMURA Corporation or third parties that these may entail.
- The circuit examples and part constants listed in this document are provided as reference for the verification of characteristics. You are to perform design, verification, and judgment at your own responsibility, taking into account the various conditions.
- TAMURA has evaluated the efficiency and performance of this product in a usage environment determined by us.
Depending on your usage environment or usage method, there is the possibility that this product will not perform sufficiently as shown in the specifications, or may malfunction.
When applying this product to your devices or systems, please ensure that you conduct evaluations of their state when integrated with this product. You are responsible for judging its applicability.
TAMURA bears no responsibility whatsoever for any problems with your devices, systems or this product which are caused by your usage environment or usage method.
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- This product is intended for use in consumer electronics (electric home appliances, business equipment, Information equipment, communication terminal equipment, measuring devices, and so on.) If considering use of this product in equipment or devices that require high reliability (medical devices, transportation equipment, traffic signal control equipment, fire and crime prevention equipment, aeronautics and space devices, nuclear power control, fuel control, in-vehicle equipment, safety devices, and so on), please consult a TAMURA sales representative in advance. Do not use this product for such applications without written permission from TAMURA Corporation.
- This product is intended for use in environments where consumer electronics are commonly used.
It is not designed for use in special environments such as listed below, and if such use is considered, you are to perform thorough safety and reliability checks at your own responsibility.
 - Use in liquids such as water, oil, chemical solutions, or organic solvents, and use in locations where the product will be exposed to such liquids.
 - Use that involves exposure to direct sunlight, outdoor exposure, or dusty conditions.
 - Use in locations where corrosive gases such as salt air, C12, H2S, NH3, SO2, or NO2, are present.
 - Use in environments with strong static electricity or electromagnetic radiation.
 - Use that involves placing inflammable material next to the product.
 - Use of this product either sealed with a resin filling or coated with resin.
 - Use of water or a water soluble detergent for flux cleaning.
 - Use in locations where condensation is liable to occur.
- This product is not designed to resist radiation.
- This product is not designed to be connected in series or parallel.
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