

## Gate Driver Unit 2DUD51008xFP3

### ■ Overview

2DUD51008xFP3 is a dual channel gate driver designed for Fuji Electric's IGBT power module 2MBI1800XXF170-50 .

This gate driver unit contains a built-in isolated DC/DC converter and gate drive circuit. It is ready to use by mounting it on the IGBT power module.

### ■ Features

- Ideal for drive of IGBT Power module 2MBI1800XXF170-50 (Fuji Electric)
- Gate voltage : +15V/-10V
- Gate resistor : +0.22Ω / -1.5Ω (TYP)
- Short circuit detection voltage : 9.5V(TYP)
- ALL-IN-ONE (Built-in isolated DC / DC converter and gate drive circuit)
- Low parasitic capacitance (12pF(TYP)) ; highly resistant to common-mode noise.
- Fast response : About 130nsec(typ)
- The isolation for primary-secondary signal used fast response isolator.
- Dielectric withstand voltage : AC5000V
- Insulation distance (clearance / creepage) : 14mm/14mm
- DC/DC converter input voltage : 13~28V
- Power supply for gate driver input voltage : 13~28V
- Signal input voltage : 3.3V~15V or 15V
- Overload protection (DC/DC converter)
- Overheat protection (DC/DC converter)
- Desaturation protection (Gate drive circuit)
- Soft turn-off function (Gate drive circuit)
- Fault signal output function (Gate drive circuit)
- Under-voltage lockout(UVLO) (Gate drive circuit)
- Direct mode / Half bridge mode can be switched. (Gate drive circuit)
- Active clamp protection function ( Gate drive circuit)
- Safety standards:UL508(file no.E243511) (DC/DC converter only)
- Reinforced isolation according to IEC 60664-1 (IEC61800-5-1, IEC62477-1, IEC62109-1, etc.)
- UL compliant (UL1741, UL508, etc.)

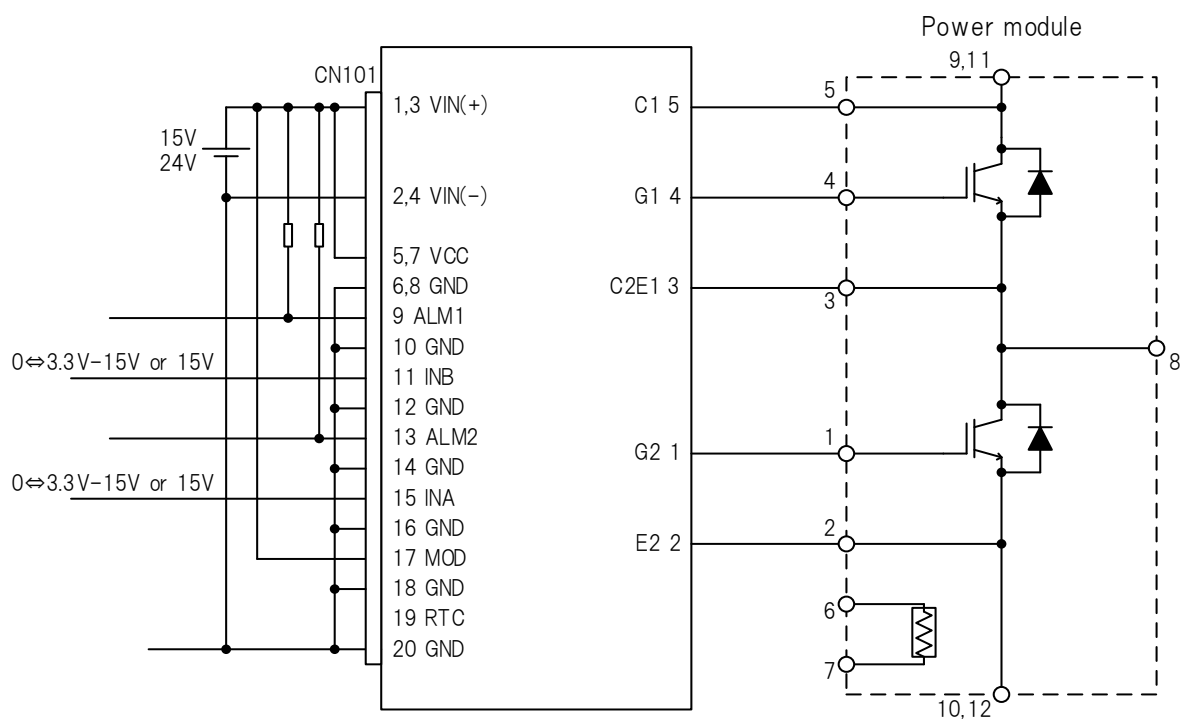
### ■ Application

Industrial inverter, power conditioner, etc ...

### ■ Module information

Part number	Signal input voltage	Active clamp	Status
2DUD51008CFP3	3.3 to 15V	Yes	Active
2DUD51008DFP3	15V	Yes	Active

## ■Circuit Image



## ■Pin Connection

CN101 : RA-H201SD (JST), 71922-120LF (FCI) or PS-20PE-D4LT2-M1E (JAE)

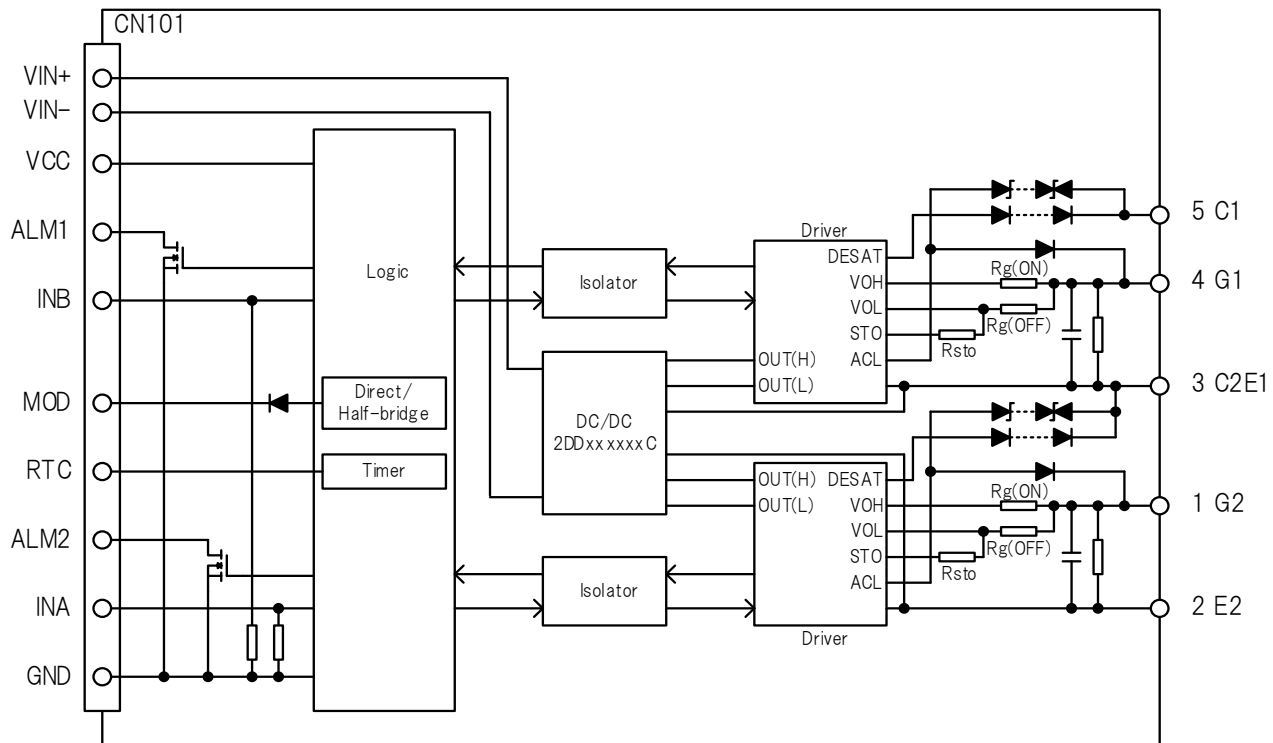
Pin No.	Name	Function	Pin No.	Name	Function
1	VIN(+)	Power supply for DC/DC converter(+)	2	VIN(-)	Power supply for DC/DC converter(-)
3	VIN(+)	Power supply for DC/DC converter(+)	4	VIN(-)	Power supply for DC/DC converter(-)
5	VCC	Power supply for drive circuit	6	GND	Ground for drive circuit
7	VCC	Power supply for drive circuit	8	GND	Ground for drive circuit
9	ALM1	Alarm signal output 1 (High side)	10	GND	Ground for drive circuit
11	INB	Control input B (High side)	12	GND	Ground for drive circuit
13	ALM2	Alarm signal output 2 (Low side)	14	GND	Ground for drive circuit
15	INA	Control input A (Low side)	16	GND	Ground for drive circuit
17	MOD	Mode select	18	GND	Ground for drive circuit
19	RTC	Recovery time of protection circuit control	20	GND	Ground for drive circuit

※Recommend receptacle : RA-S201T (JST), 71600-020LF(FCI) or PS-20SM-D4P1-1\*(JAE)

Connection on the power module

Pin No.	Name	CH	Function
1	G2	2(L)	Gate connection, Low side
2	E2	2(L)	Emitter connection, Low side
3	C2E1	1(H)	Emitter connection, High side
4	G1	1(H)	Gate connection, High side
5	C1	1(H)	Collector connection, High side

## ■ Internal Block Diagram



## ■ I/O Condition Table

No.	Status	Input							Output			
		OUT(H)	V <sub>CC</sub>	C1(H)	C2(L)	MOD	INB	INA	ALM1	ALM2	G1(H)	G2(L)
1	V <sub>OUT</sub> UVLO	UVLO	○	X	X	X	X	X	L	L	L	L
2	G-E short	○	○	X	X	X	X	X	Hi-Z	Hi-Z	SD	SD
3	V <sub>CC</sub> UVLO	○	UVLO	X	X	X	X	X	Hi-Z	Hi-Z	L	L
4	Normal operation	○	○	L	L	H	X	L	Hi-Z	Hi-Z	X	L
5		○	○	L	L	H	X	H	Hi-Z	Hi-Z	X	H
6		○	○	L	L	H	L	X	Hi-Z	Hi-Z	L	X
7		○	○	L	L	H	H	X	Hi-Z	Hi-Z	H	X
8		○	○	L	L	L	L	X	Hi-Z	Hi-Z	L	L
9		○	○	L	L	L	H	L	Hi-Z	Hi-Z	H	L
10		○	○	L	L	L	H	H	Hi-Z	Hi-Z	L	H
11	Short circuit detection (L)	○	○	L	Hi-Z	H	X	L	Hi-Z	Hi-Z	X	L
12		○	○	L	Hi-Z	H	X	H	Hi-Z	L	X	L
13		○	○	L	Hi-Z	L	H	L	Hi-Z	Hi-Z	H	L
14		○	○	L	Hi-Z	L	H	H	Hi-Z	L	L	L
15	Short circuit detection (H)	○	○	Hi-Z	L	H	L	X	Hi-Z	Hi-Z	L	X
16		○	○	Hi-Z	L	H	H	X	L	Hi-Z	L	X
17		○	○	Hi-Z	L	L	H	L	L	Hi-Z	L	L
18		○	○	Hi-Z	L	L	H	H	Hi-Z	Hi-Z	L	H

G-E short : Gate-Emitter short

○ : OUT(H) UVLO &gt; UVLO, X : Don't care

SD: Shut down (Gate-Emitter short)

**■ Absolute Maximum Ratings**

Item	Symbol	Min	Max	Unit	Conditions·Note	
Input voltage for DC/DC converter	$V_{IN}$	-0.3	28	Vdc	Between VIN(+) to VIN(-)	
Input voltage for Gate driver	$V_{CC}$	-0.3	28	Vdc	Between VCC to GND	
Input-side signal voltage	$V_{SG}$	-0.3	$V_{CC}+0.3$ or 18 *	V	INA, INB *Whichever is less	
	$V_{MOD}$	-0.3	28	V	MOD	
	$V_{RTC}$	-0.3	5	V	RTC	
Maximum gate current	$I_{GPEAK}$	-	43	A	Excluding gate resistor	
Switching frequency	$F_{SW}$	-	20	kHz	See the permissible frequency curve	
Short circuit detection pin voltage	$V_{SD}$	0	1700	V		
Alarm signal output pin maximum voltage	$V_{ALM}$	-0.3	$V_{CC}+0.3$ or 28 *	V	ALM1,2 *Whichever is less	
Input-side signal maximum current	$I_{ALM}$	-	5	mA	ALM1,2	
Operating temperature range	$V_{IN}=13.5-18V$	$T_{OP}$	-40	85	°C	See the permissible frequency curve
	$V_{IN}=18-26.4V$	$T_{OP}$	-40	75	°C	
Operating humidity	$RH_{OP}$	20	95	%RH	No condensation	
Storage temperature range	$T_{STG}$	-40	90	°C		
Storage humidity	$RH_{STG}$	5	95	%RH	No condensation	

**■ Recommended Operating Conditions**

Item	Symbol	Min	Max	Unit	Conditions·Note
Input voltage range for DC/DC converter	$V_{IN}$	13.5	26.4	Vdc	
Input voltage range for gate driver	$V_{CC}$	13.5	26.4	Vdc	
Driver circuit number	N	-	2	-	
Maximum gate charge	$Q_G$	-	14000	nC	*1
Switching frequency ( $Q_g=12500nC$ )	$F_{SW}$	-	8.8	kHz	See the permissible frequency curve
MOD pin high input voltage	$V_{MODH}$	3.3	26.4	V	
MOD pin low input voltage	$V_{MODL}$	-0.3	0.5	V	
2DUx51008Cxxx					
Logic high level input voltage	$V_{SGH}$	3.3	$V_{CC}+0.3$ or 16 *	V	INA, INB *Whichever is less
Logic low level input voltage	$V_{SGL}$	-0.3	0.5	V	INA, INB
Source current of control signal	$I_{SG}$	3.2	-	mA	INA, INB $V_{SG}=15V$
2DUx51008Dxxx					
Logic high level input voltage	$V_{SGH}$	13	$V_{CC}+0.3$ or 16 *	V	INA, INB *Whichever is less
Logic low level input voltage	$V_{SGL}$	-0.3	0.5	V	INA, INB
Source current of control signal	$I_{SG}$	3.3	-	mA	INA, INB $V_{SG}=15V$
For 2DUD51008xxxx(Active clamp model)					
DC-link voltage	$V_{DC-LINK}$	-	1100	V	

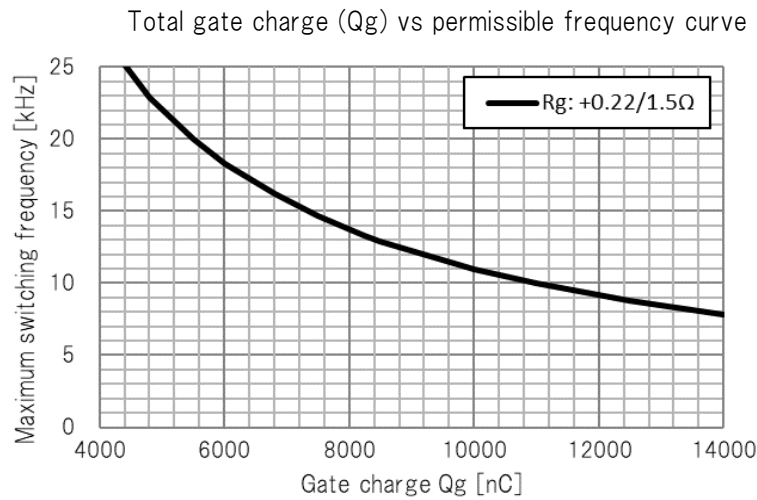
\*1 If the gate charge exceeds the allowable value, the gate voltage at turn-on and turn-off will drop, which may affect the switching performance of the IGBT.

If you are considering using it under conditions other than the recommended conditions, please contact us.

**■ Permissible frequency curve**

Internal gate resistor of IGBT = 2.08 Ω

\*The permissible frequency curve changes with the ratio of the IGBT internal gate resistance to the gate resistance.  
Therefore, as the internal resistance of the IGBT decreases, the allowable frequency also decreases.



Ta: -40~+85°C / VIN = 13.5~18V

Ta: -40~+75°C / VIN = 18~26.4V

**■ Electrical Specification (Vin=Vcc=15V, Ta=25°C, Unless otherwise specified)**

Item	Symbol	Min	Typ	Max	Unit	Conditions·Note	
DC/DC converter							
Start-up voltage	V <sub>START</sub>	-	11.5	12.5	V		
Input current	I <sub>IN</sub>	-	0.5	-	A	F <sub>sw</sub> =10kHz, Test load : 2.15 Ω / 440nF	
Standby power	P <sub>STBY</sub>	-	1.3	-	W	No load	
Logic inputs for 2DUx51008Cxxx							
Logic high level input voltage	V <sub>SGH</sub>	-	2.1	2.3	V	INA, INB / Guaranteed by design	
Logic low level input voltage	V <sub>SGL</sub>	1.0	1.2	-	V	INA, INB / Guaranteed by design	
Logic pull-down resistance	R <sub>SGD</sub>	-	4700	-	Ω	INA, INB	
Logic inputs for 2DUx51008Dxxx							
Logic high level input voltage	V <sub>SGH</sub>	-	10.4	11.4	V	INA, INB / Guaranteed by design	
Logic low level input voltage	V <sub>SGL</sub>	4.9	5.9	-	V	INA, INB / Guaranteed by design	
Logic pull-down resistance	R <sub>SGD</sub>	-	4500	-	Ω	INA, INB	
Gate driver output							
Output pin voltage(High)	V <sub>OUTH</sub>	14	15	16	V	No load	
Output pin voltage(Low)	V <sub>OUTL</sub>	-11	-10	-9	V	No load	
Gate resistor	Rg(ON)	-	0.22	-	Ω		
	Rg(OFF)	-	1.5	-			
Auxiliary gate capacitor	C <sub>ge</sub>	-	OPEN	-	nF		
Delay time	Turn ON time	t <sub>PON</sub>	-	130	-	ns	
	Turn OFF time	t <sub>POFF</sub>	-	130	-	ns	
Dead time	t <sub>DEAD</sub>	-	3	-	us	Half bridge mode	

**Protection**

Item	Symbol	Min	Typ	Max	Unit	Conditions·Note
DC/DC converter						
Overload protection	-	10.5	-	-	W	Auto recovery
Overheat protection	-	120	-	150	°C	Auto recovery, Internal temperature
Gate driver						
VCC UVLO OFF voltage	$V_{UVLOVCH}$	-	9.1	-	V	
VCC UVLO ON voltage	$V_{UVLOVCL}$	-	7.3	-	V	
Output voltage(H) UVLO OFF voltage	$V_{UVLOOH}$	13.2	13.5	13.8	V	Guaranteed by design
Output voltage(H) UVLO ON voltage	$V_{UVLOOH}$	12.2	12.5	12.8	V	Guaranteed by design
Short circuit detection voltage	$V_{SD}$	-	9.5	-	V	
Short circuit detection filter time	$t_{SHORTFIL}$	-	4.5	-	us	Collector open
Alarm signal output L voltage	$V_{ALML}$	-	-	0.5	V	$I_{ALM}=5mA$
Alarm signal output time	$t_{ALM}$	-	0.2	-	us	
Restart time	$t_{RESTART}$	-	110	-	ms	
Soft turn-off resistance	$R_{STO}$	-	12	-	$\Omega$	
Soft turn-off duration	$t_{STO}$	-	4	-	us	

**Insulation**

Item	Specification	Conditions·Note
Between Input-Output		
Dielectric withstand voltage	AC5000V	1min, Cutoff 2mA
Insulation resistance	100M $\Omega$ or more	DC500V
Partial discharge extinction voltage	1768V <sub>peak</sub> or more	According to EN50178/IEC 60270
Common-mode transient immunity (CMTI)	70kV/us	
Minimum clearance distances	14mm	
Minimum creepage distances	14mm	
Between CH1-CH2		
Minimum clearance distances	8mm	
Minimum creepage distances	12mm	

**Pin Function**

·VIN(+), VIN(-) (Power supply pin for DC/DC converter)

·VCC(Power supply pin for drive circuit)

·GND(Ground pin for drive circuit)

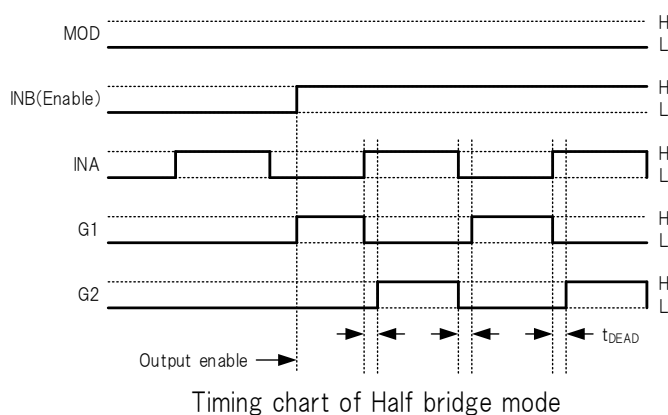
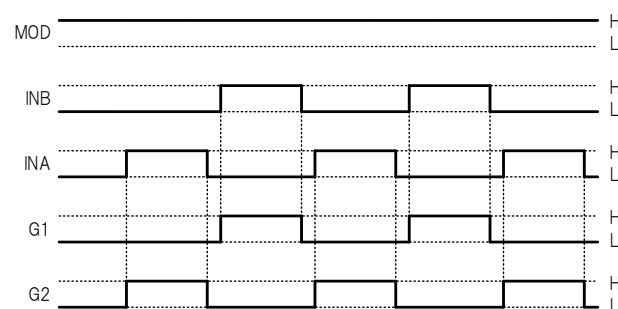
·MOD, INA, INB(Mode switching pin, Control input pin)

The INA, INB and MOD pin is a pin used to determine output logic.

Direct mode / Half bridge mode can be switched by MOD pin.

In Half bridge mode, it functions as INA: gate signal, INB: enable signal.

MOD	INB	INA	G1(H)	G2(L)	Mode
H (Floating or Connected to VCC)	X	L	X	L	Direct mode
	X	H	X	H	
	L	X	L	X	
	H	X	H	X	
L (Connected to GND)	L	X	L	L	Half bridge mode
	H	L	H	L	
	H	H	L	H	



·RTC(Recovery time of protection circuit control pin)

When abnormality occurs (UVLO, short circuit detected),this pin is used to adjust the recovery time.

·ALM1,2(Alarm signal output pin)

When abnormality occurs (UVLO, short circuit detected),This pin outputs an alarm signal. (Open drain)

Status	ALM1,2
While in normal operation	Hi-Z
UVLO, When detecting short circuit	L

**Description**

## 1. Undervoltage Lockout (UVLO) function

The control circuit incorporates the undervoltage lockout (UVLO) function both on the VCC and the OUT(H) sides.

When the OUT(H) voltage drops to the UVLO ON voltage, the Output pin and the ALM pin both will output the “L” signal.

When the VCC voltage drops to the UVLO ON voltage, the Output pin will output the “L” signal.

When the VCC or the OUT(H) voltage rises to the UVLO OFF voltage, these pins will be reset.

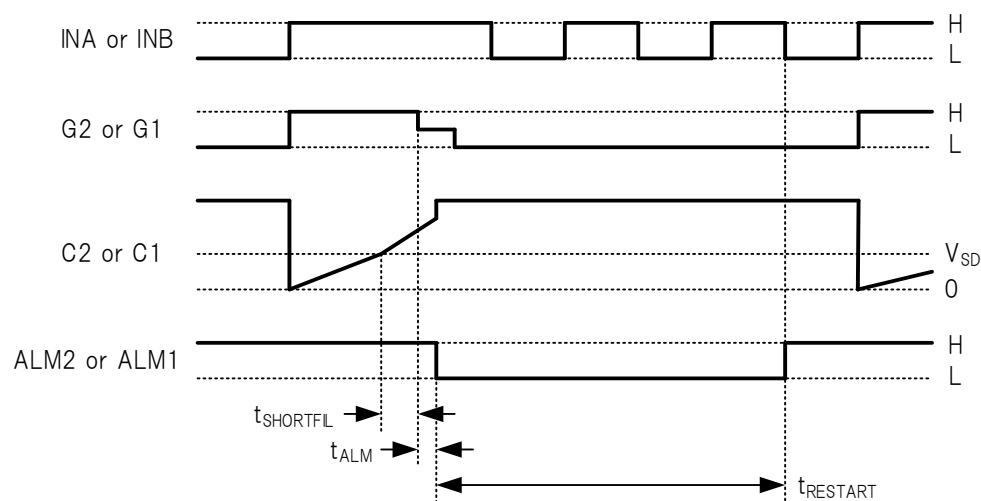
## 2. Short circuit protection function, Soft turn-off function

When the collector pin voltage exceeds  $V_{SD}$ , the short circuit protection function will be activated.

When the short circuit protection function is activated, the OUT pin voltage will be set to the “L” level, and then the ALM pin voltage to the “L” level.

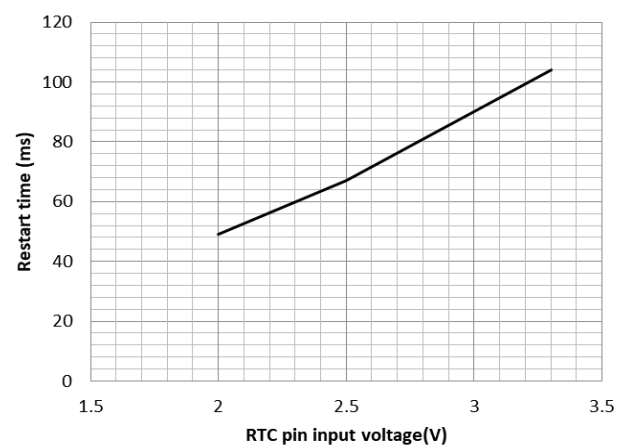
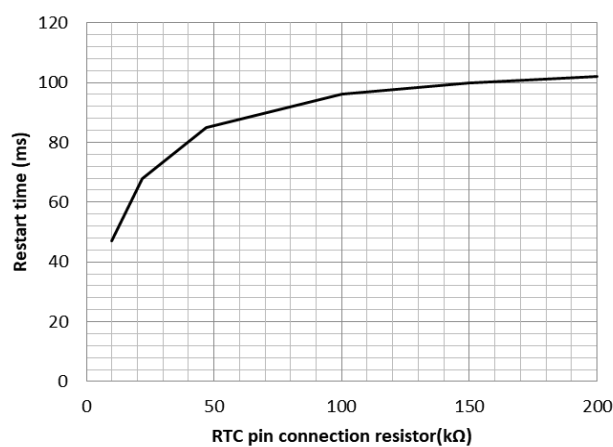
Also, soft turn-off function works to reduce collector voltage surge due to short circuit current.

Short-circuit protection is canceled automatically after an abnormal condition restart time and when the input signal is “L” level.



Timing chart of short circuit protection function

The restart time can be adjusted within the following range by the resistance or voltage connected to the RTC pin.





**■Description**

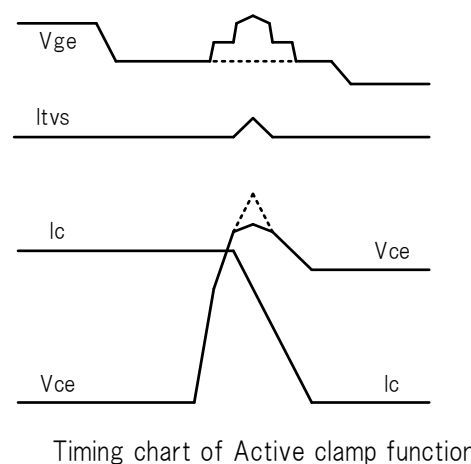
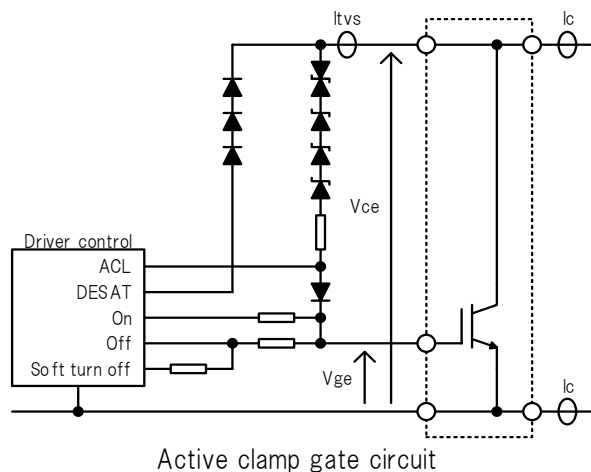
## 3. Active clamp function (2DUD only)

The active clamp function protects the IGBT from a sudden voltage surge between the collector and emitter that occurs when the IGBT is turned off. Depending on the DC-LINK voltage and surge voltage, the losses that occur in TVS diodes and IGBTs increase. If a high surge voltage is clamped continuously, stress will be applied to the driver and IGBT. Therefore, the main circuit conditions (DC-LINK voltage, parasitic inductance,  $dlc / dt$ ) should be optimized and designed so that the active clamp function does not operate normally. Use the TVS diode with a surface temperature of 120 °C or less.

When the collector–gate voltage exceeds the breakdown voltage of the active clamp TVS diode, current flows from the collector to the gate. The current ( $I_{tvs}$ ) supplies the gate with charge, partially turning on the IGBT and clamping the collector voltage.

TVS loss can be obtained by time integration of the current ( $I_{tvs}$ ) and collector–emitter voltage ( $V_{ce}$ ).

To strengthen the clamp, the gate may rise even during the period when no current is flowing through the TVS diode, but this is not a malfunction.

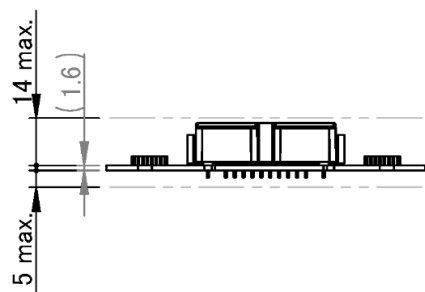
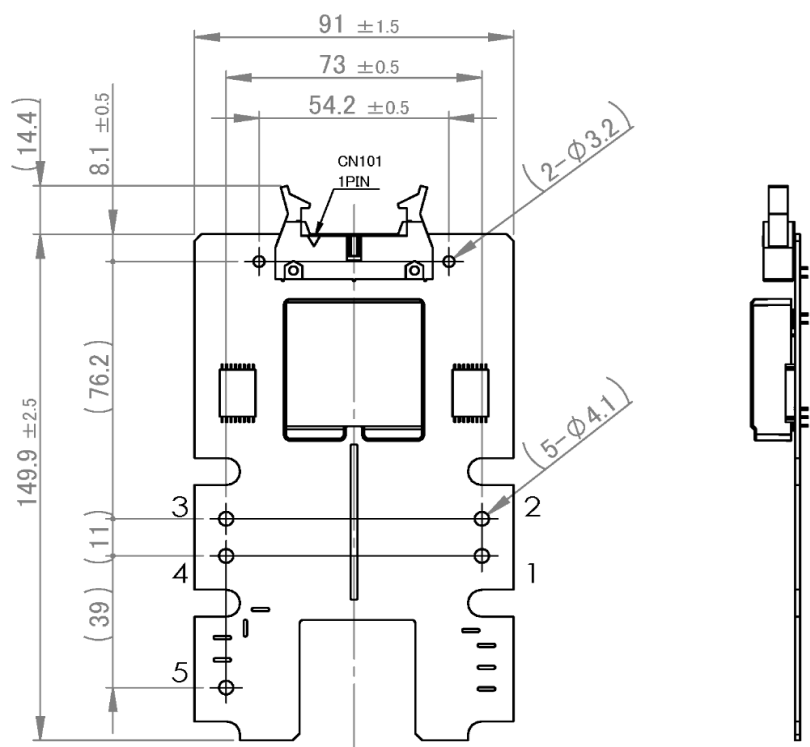

**■Reliability**

Item	Test condition and acceptance criterion
Exposure in high temperature	IEC60068-2-2:2007: Test: Bb, 90°C, 240H, ※
Exposure in low temperature	IEC60068-2-1:2007: Test: Ab, -40°C, 240H, ※
Exposure in high temperature and high humidity	IEC60068-2-78:2012-10, 60°C, 90~95%RH, 240H, ※
Thermal shock	IEC60068-2-14:2009-01: Test: Na, -40°C/30min to 100°C/30min, 500cycles, ※
Low temperature operation	IEC60068-2-1:2007: Test: Ae, Input voltage: DC24V, Output current: Rated Load -40°C, 240H, ※
High temperature operation	IEC60068-2-2:2007: Test: Be, Input voltage: DC24V, Output current: Rated Load 85°C, 240H, ※
high temperature and high humidity operation	IEC60068-2-78:2012-10, Input voltage: DC24V, Output current: Rated Load 85°C, 85%RH, 240H, ※
Vibration	IEC 60068-2-6:2007-12, Frequency range: 10 to 55Hz, Sweep rate: 1.0oct/min, Displacement amplitude: ±0.75mm, Test duration: 120min, Axis: X,Y and Z, ※
	IEC 60068-2-6:2007-12, Frequency range: 5 to 200Hz, Sweep rate: 1.0oct/min, Cross-over frequency: 8.4Hz, Displacement amplitude(5 to 8.4Hz): ±3.5mm, Acceleration amplitude(8.4 to 200Hz):1G, Test duration: 20sweeps, Axis: X, Y and Z, ※
Impact	IEC 60068-2-27:2008-02, Pulse shape: Half-sine, Corresponding duration: 11ms, Peak acceleration: 50G, Number of shocks:3, Axis: ±X, Y and Z, ※
	IEC 60068-2-27:2008-02, Pulse shape: Half-sine, Corresponding duration: 6ms, Peak acceleration: 15G, Number of shocks:100, Axis: ±X, Y and Z, ※

※After each test, exposure at room temperature and humidity condition for 24 hours.

There shall be no abnormality on the electrical specification and appearance.

■ Outline Dimensional Drawing



Unit: mm

Note :1.The dimensional tolerance without directions is  $\pm 0.5$ mm.

■ Product Weight

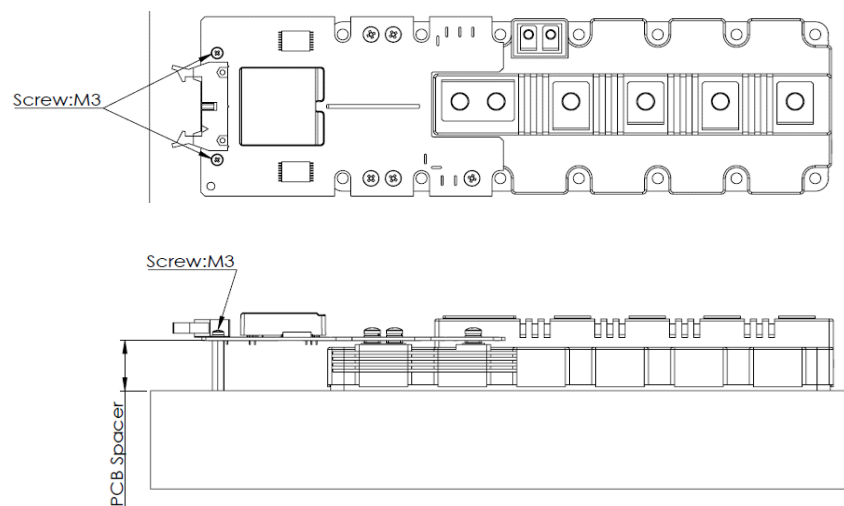
81.0g(typ)

**■Storage Conditions**

Item	Min	Max	Unit	Conditions·Note
Storage temperature	-25	60	°C	A packing state

**■Usage Cautions**

- Always mount fuse on the plus side of input for ensuring safety because the fuse is not built-in the product.  
Please select the fuse considering conditions such as steady current, inrush current, and ambient temperature.  
When using a fuse having large rated current or high capacity input electrolytic condenser, by combining another converter and input line and input electrolytic condenser, fuse may not blow off in the case of abnormality.  
Do not combine high voltage line and fuse.
  
- Make sure the rise/fall time of the input signal is 500ns or less.  
Also, keep input wiring as far as possible from noise sources.  
To prevent malfunction due to noise, we recommend the highest possible signal voltage within the recommended range.
  
- Please do not apply excessive stress to this product when attaching to IGBT power module.  
Please follow the device manufacturer's instructions on how to install the IGBT power module (type of screw used, material, tightening torque conditions, etc.).  
Also, if the product is exposed to vibration or shock, the PCB should be fixed with spacers as shown in the figure below.



- This product has DESAT protection for arm short circuit and load short circuit protection.  
However, even if this protection works, the IGBT may be damaged if abnormally high current occurs due to IGBT's characteristics variations or the load short-circuit mode during parallel operation.  
To ensure safety, be sure to check the short-circuit current at the unit in which this product is integrated, and evaluate whether it can protect under the condition that there is no damage to the IGBT.

**■ Important Notice**

- This information and product are subject to change without prior notice for the purpose of improvements, etc.  
Ensure that you are in possession of the most up-to-date information when using this product.
- The operation examples and circuit examples shown in this document are for reference purposes only, and TAMURA Corporation disclaims all responsibility for any violations of industrial property rights, intellectual property rights and any other rights owned by TAMURA Corporation or third parties that these may entail.
- The circuit examples and part constants listed in this document are provided as reference for the verification of characteristics. You are to perform design, verification, and judgment at your own responsibility, taking into account the various conditions.
- TAMURA has evaluated the efficiency and performance of this product in a usage environment determined by us.  
Depending on your usage environment or usage method, there is the possibility that this product will not perform sufficiently as shown in the specifications, or may malfunction.  
When applying this product to your devices or systems, please ensure that you conduct evaluations of their state when integrated with this product. You are responsible for judging its applicability.  
TAMURA bears no responsibility whatsoever for any problems with your devices, systems or this product which are caused by your usage environment or usage method.
- TAMURA Corporation constantly strives to improve quality and reliability, but malfunction or failures are bound to occur with some probability in power products. To ensure that failures do not cause accidents resulting in injury or death, fire accidents, social damage, and so on, you are to thoroughly verify the safety of their designs in devices and/or systems, at your own responsibility.
- This product is intended for use in consumer electronics (electric home appliances, business equipment, Information equipment, communication terminal equipment, measuring devices, and so on.) If considering use of this product in equipment or devices that require high reliability (medical devices, transportation equipment, traffic signal control equipment, fire and crime prevention equipment, aeronautics and space devices, nuclear power control, fuel control, in-vehicle equipment, safety devices, and so on), please consult a TAMURA sales representative in advance. Do not use this product for such applications without written permission from TAMURA Corporation.
- This product is intended for use in environments where consumer electronics are commonly used.  
It is not designed for use in special environments such as listed below, and if such use is considered, you are to perform thorough safety and reliability checks at your own responsibility.
  - Use in liquids such as water, oil, chemical solutions, or organic solvents, and use in locations where the product will be exposed to such liquids.
  - Use that involves exposure to direct sunlight, outdoor exposure, or dusty conditions.
  - Use in locations where corrosive gases such as salt air, C12, H2S, NH3, SO2, or NO2, are present.
  - Use in environments with strong static electricity or electromagnetic radiation.
  - Use that involves placing inflammable material next to the product.
  - Use of this product either sealed with a resin filling or coated with resin.
  - Use of water or a water soluble detergent for flux cleaning.
  - Use in locations where condensation is liable to occur.
- This product is not designed to resist radiation.
- This product is not designed to be connected in series or parallel.  
Do not operate this product in a series, parallel, or N+1 redundant configuration.
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