

# Gate Driver Unit 4DUC51016xFA2 for 3-Level IGBT

#### **■**Overview

4DUC51016xFA2 is a quad channel gate driver designed for Fuji Electric's

IGBT power module 4MBI900VB-120RA-50.

This gate driver unit contains a built-in isolated DC/DC converter and gate drive circuit.

It is ready to use by mounting it on the IGBT power module.

#### **■**Features

- ·Ideal for drive of 3-Level inverter
- ·Ideal for drive of IGBT Power module 4MBI900VB-120RA-50 (Fuji Electric)
- ·Gate voltage: +15V/-10V
- •Gate resistor (T1,T4) :  $+3.9 \Omega / -1.0 \Omega (TYP)$
- •Gate resistor (T2,T3):  $+2.7 \Omega /-15 \Omega (TYP)$
- ·Short circuit detection voltage : 10V(TYP)
- $\cdot$ ALL-IN-ONE (Built-in isolated DC / DC converter and gate drive circuit)
- ·Low parasitic capacitance (12pF(TYP)); highly resistant to common-mode noise.
- ·Fast response : About 130nsec(typ)
- The isolation for primary-secondary signal used fast response isolator.
- ·Dielectric withstand voltage: AC5000V
- ·Insulation distance (clearance / creepage): 14mm/14mm
- ·DC/DC converter input voltage : 13~28V
- ·Power supply for gate driver input voltage: 13~28V
- ·Signal input voltage: 3.3V~15V or 15V
- Overload protection (DC/DC converter)
- Overheat protection (DC/DC converter)
- ·Desaturation protection (Gate drive circuit / T1, T4)
- ·Soft turn-off function (Gate drive circuit / T1, T4)
- ·Fault signal output function (Gate drive circuit)
- Under-voltage lockout(UVLO) (Gate drive circuit)
- ·Safety standards: UL508(file no.E243511) (DC/DC converter only)
- ·UL1741, UL508, IEC62109-1, EN50178 compliant (Reinforced isolation according to IEC 60664-1)

### ■ Application

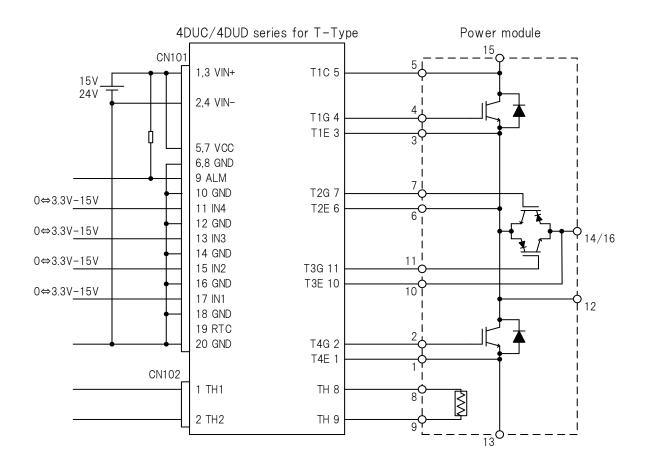
Industrial inverter, power conditioner, etc ...

#### ■ Module information

Part number	Signal input voltage	Active clamp	Status
4DUC51016CFA2	3.3 to 15V	None	Active
4DUC51016DFA2	15V	None	Sample



#### **■**Circuit Image



#### **■**Pin Connection

CN101: RA-H201SD (JST), 71922-120LF (FCI) or PS-20PE-D4LT2-M1E (JAE)

Pin No.	Name	Function	Pin No.	Name	Function
1	VIN(+)	Power supply for DC/DC converter(+)	2	VIN(-)	Power supply for DC/DC converter(-)
3	VIN(+)	Power supply for DC/DC converter(+)	4	VIN(-)	Power supply for DC/DC converter(-)
5	VCC	Power supply for drive circuit	6	GND	Ground for drive circuit
7	VCC	Power supply for drive circuit	8	GND	Ground for drive circuit
9	ALM	Alarm signal output	10	GND	Ground for drive circuit
11	IN4	Control input 4	12	GND	Ground for drive circuit
13	IN3	Control input 3	14	GND	Ground for drive circuit
15	IN2	Control input 2	16	GND	Ground for drive circuit
17	IN1	Control input 1	18	GND	Ground for drive circuit
19	RTC	Recovery time of protection circuit control	20	GND	Ground for drive circuit

\*\*Recommend receptacle: RA-S201T (JST), 71600-020LF(FCI) or PS-20SM-D4P1-1\*(JAE)

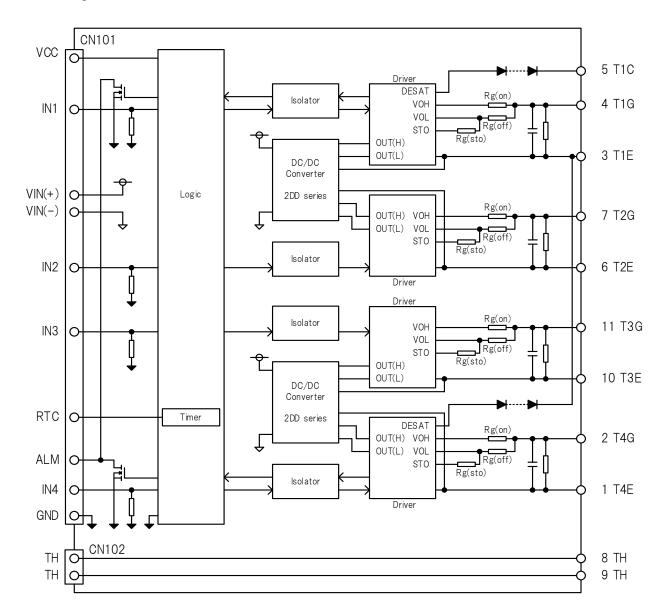
CN102: S2B-XH-A For thermistor %Recommend receptacle: XHP-2 (JST)

#### Connection on the power module

Pin No.	Name	Function	Pin No.	Name	Function
1	T4E	T4 Emitter connection	6	T2E	T2 Emitter connection
2	T4G	T4 Gate connection	7	T2G	T2 Gate connection
3	T1E	T1 Emitter connection	8	TH	For thermistor
4	T1G	T1 Gate connection	9	TH	For thermistor
5	T1C	T1 Collector connection	10	T3E	T3 Emitter connection
			11	T3G	T3 Gate connection



#### ■Internal Block Diagram



#### ■I/O Condition Table

No.	Status				Inp	out						Output	:	
INO.	Status	OUT(H)	$V_{CC}$	T1C	T4C	IN1	IN2	IN3	IN4	ALM	T1G	T2G	T3G	T4G
1	V <sub>OUT</sub> UVLO	UVLO	0	Χ	Χ	Χ	Χ	Χ	Χ	L	L	L	L	L
2	G-E short	0	0	Χ	Χ	Χ	Χ	Χ	Χ	Hi-Z	SD	SD	SD	SD
3	VccUVLO	0	UVLO	Χ	Χ	Χ	Χ	Χ	Χ	Hi-Z	L	L	L	L
4		0	0	L	L	L	Χ	Χ	Χ	Hi-Z	L	Χ	Χ	Χ
5		0	0	L	L	Н	Χ	Χ	Χ	Hi-Z	Н	Χ	Χ	Χ
6		0	0	L	L	Χ	L	Χ	Χ	Hi-Z	Χ	L	Χ	Χ
7	Normal	0	0	L	L	Χ	Н	Χ	Χ	Hi-Z	Χ	Н	Χ	Χ
8	operation	0	0	L	L	Χ	Χ	L	Χ	Hi-Z	Χ	Χ	L	Χ
9		0	0	L	L	Χ	Χ	Н	Χ	Hi-Z	Χ	Χ	Н	Χ
10		0	0	L	L	Χ	Χ	Χ	L	Hi-Z	Χ	Χ	Χ	L
11		0	0	L	L	Χ	Χ	Χ	Н	Hi-Z	Χ	Χ	Χ	Н
12	Short circuit detection	0	0	Hi-Z	L	L	Χ	Χ	Χ	Hi-Z	L	Χ	Χ	Χ
13	(T1)	0	0	Hi-Z	L	Н	Χ	Χ	Χ	L	L	Χ	Χ	Χ
14	Short circuit detection	0	0	L	Hi-Z	Χ	Χ	Χ	Ĺ	Hi-Z	Χ	Χ	Χ	L
15	(T4)	0	0	L	Hi-Z	Χ	Χ	Χ	Н	L	Χ	Χ	Χ	L

G-E short : Gate-Emitter short

O: OUT(H) UVLO > UVLO, X: Don't care

SD: Shut down (Gate-Emitter short)



### ■ Absolute Maximum Ratings

, 15001410 1114711114111 1 (4					_		
lte	em		Symbol	Min	Max	Unit	Conditions · Note
Input voltage for DC/	DC cor	nverter	$V_{IN}$	-0.3	28	Vdc	Between VIN(+) to VIN(-)
Input voltage for Gate	e drive	ſ	V <sub>CC</sub>	-0.3	28	Vdc	Between VCC to GND
Input-side si	ignal v	oltage	$V_{SG}$	-0.3	V <sub>CC</sub> +0.3 or 18 *	V	IN1, IN2, IN3, IN4 *Whichever is less
			$V_{RTC}$	-0.3	5	V	RTC
Maximum gate curren	t		I <sub>GPEAK</sub>	-	43	Α	Excluding gate resistor
Cuitabing fraguancy	Average		Г	-	7.5	kHz	Test load (T1,T4): 0.8 Ω / 340nF
Switching frequency	Peak		F <sub>SW</sub>	-	15	kHz	Test load (T2,T3) : 2.8 Ω / 162nF
Short circuit detection	n pin v	oltage	$V_{SD}$	0	1200	V	
Alarm signal output pi	n maxi	mum voltage	$V_{ALM}$	-0.3	V <sub>CC</sub> +0.3 or 28 *	V	ALM1,2 *Whichever is less
Input-side signal maxi	imum c	urrent	I <sub>ALM</sub>	-	5	mA	ALM
Operating temperature	K00000	V <sub>IN</sub> =13.5−18V	T <sub>OP</sub>	-40	85	°C	Coo the deveting aurus
Operating temperature range $V_{IN}=18-26$		V <sub>IN</sub> =18-26.4V	T <sub>OP</sub>	-40	75	°C	See the derating curve
Operating humidity		RH <sub>OP</sub>	20	95	%RH	No condensation	
Storage temperature range		$T_{STG}$	-40	90	°C		
Storage humidity			RH <sub>STG</sub>	5	95	%RH	No condensation
Storage humidity			$RH_{STG}$	5	95	%RH	No condensation

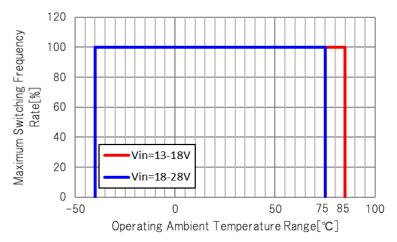
### ■Recommended Operating Conditions

ltem	Symbol	Min	Max	Unit	Conditions · Note
Input voltage range for DC/DC converter	V <sub>IN</sub>	13.5	26.4	Vdc	
Input voltage range for gate driver	V <sub>cc</sub>	13.5	26.4	Vdc	
Driver circuit number	N	_	4	-	
Maximum gata sharga	0	_	8500	nC	
Maximum gate charge T2, T3	$Q_{G}$	_	3900	nC	
4DUx51016Cxxx	•				
Logic high level input voltage	V <sub>SGH</sub>	3.3	Vcc+0.3 or 16 *	V	IN1, IN2, IN3, IN4 *Whichever is less
Logic low level input voltage	$V_{SGL}$	-0.3	0.5	V	IN1, IN2, IN3, IN4
Source current of control signal	I <sub>SG</sub>	3.2	_	mA	IN1, IN2, IN3, IN4 VSG=15V
4DUx51016Dxxx	•				•
Logic high level input voltage	V <sub>SGH</sub>	13	Vcc+0.3 or 16 *	V	IN1, IN2, IN3, IN4 *Whichever is less
Logic low level input voltage	$V_{SGL}$	-0.3	0.5	V	IN1, IN2, IN3, IN4
Source current of control signal	I <sub>SG</sub>	3.3	_	mA	IN1, IN2, IN3, IN4 VSG=15V



### ■ Ambient Temperature Derating Curve

Reduce the switching frequency according to the following temperature derating table. Switching Frequency 100% = 7.5 kHz



### ■Electrical Specification (Vin=Vcc=15V.Ta=25°C, Unless otherwise specified)

	Item	Symbol	Min	Тур	Max	Unit	Conditions · Note
DC/DC conver	ter	•					
Start-up voltag	ge	$V_{START}$	_	11.5	12.5	V	
Input current		I <sub>IN</sub>	-	0.5	-	Α	Fsw=7.5kHz, Test load(T1,T4) : $0.8 \Omega/340nF$ Fsw=7.5kHz, Test load(T2,T3) : $2.8 \Omega/162nF$
Standby power		$P_{STBY}$	_	2.5	_	W	No load
Logic inputs fo	r 4DUx51016Cxxx						
Logic high leve	l input voltage	$V_{SGH}$	_	2.1	2.3	V	IN1, IN2, IN3, IN4 / Guaranteed by design
Logic low level	input voltage	$V_{SGL}$	1	1.2	_	V	IN1, IN2, IN3, IN4 / Guaranteed by design
Logic pull-dow	n resistance	$R_{SGD}$	-	4700	-	Ω	IN1, IN2, IN3, IN4
Logic inputs fo	r 4DUx51016Dxxx			-			
Logic high leve	l input voltage	$V_{SGH}$	-	10.4	11.4	V	IN1, IN2, IN3, IN4 / Guaranteed by design
Logic low level	input voltage	$V_{SGL}$	4.9	5.9	1	V	IN1, IN2, IN3, IN4 / Guaranteed by design
Logic pull-dow	n resistance	$R_{SGD}$	-	4500	-	Ω	IN1, IN2, IN3, IN4
Gate driver out	tput						
Output pin volta	age(High)	V <sub>OUTH</sub>	14	15	16	V	No load
Output pin volt	age(Low)	V <sub>OUTL</sub>	-11	-10	-9	V	No load
	T1, T4	Rg(ON)	-	3.9	ı	Ω	
Gate resistor		Rg(OFF)	_	1.0	-	JL.	
4410 10010101	T2, T3	Rg(ON)	_	2.7	_	Ω	
	12, 10	Rg(OFF)	_	15	_	JL.	
Auxiliary gate	T1, T4	Cge	_	OPEN	_	nF	
capacitor	T2, T3	Cge	_	OPEN	_	nF	
Delay time	Turn ON time	t <sub>PON</sub>	-	130	_	ns	
Dolay tillic	Turn OFF time	t <sub>POFF</sub>	-	130	1	ns	



# $\blacksquare$ Protection

ltem	Symbol	Min	Тур	Max	Unit	Conditions · Note
DC/DC converter						
Overload protection	-	10.5	-	-	W	Auto recovery
Overheat protection	-	120	-	150	°C	Auto recovery, Internal temperature
Gate driver						
VCC UVLO OFF voltage	V <sub>UVLOVCCH</sub>	_	9.1	_	V	
VCC UVLO ON voltage	V <sub>UVLOVCCL</sub>	-	7.3	-	V	
Output voltage(H) UVLO OFF voltage	$V_{\rm UVLOOHH}$	13.2	13.5	13.8	V	Guaranteed by design
Output voltage(H) UVLO ON voltage	$V_{\rm UVLOOHL}$	12.2	12.5	12.8	V	Guaranteed by design
Short circuit detection voltage	$V_{SD}$	-	10	_	V	
Short circuit detection filter time	t <sub>SHORTFIL</sub>	_	4.5	-	us	Collector open
Alarm signal output L voltage	$V_{ALML}$	-	-	0.5	V	I <sub>ALM</sub> =5mA
Alarm signal output time	t <sub>ALM</sub>	-	0.2	-	us	
Restart time	t <sub>RESTART</sub>	-	110	_	ms	
Soft turn-off resistance	R <sub>STO</sub>	-	15	_	Ω	
Soft turn-off duration	t <sub>sto</sub>	-	4	-	us	

#### ■Insulation

ltem	Specification	Conditions · Note
Between Input-Output	<del>!</del>	•
Dielectric withstand voltage	AC5000V	1min, Cutoff 2mA
Insulation resistance	$100M\Omega$ or more	DC500V
Partial discharge extinction voltage	1768Vpeak or more	According to EN50178/IEC 60270
Common-mode transient immunity (CMTI)	70kV/us	
Minimum clearance distances	14mm	
Minimum creepage distances	14mm	
Between Output-Output		
Minimum clearance distances	8mm	
Minimum creepage distances	8mm	
Between Thermistor-Output		•
Minimum clearance distances	8mm	
Minimum creepage distances	8mm	



#### ■Pin Function

- ·VIN(+), VIN(-) (Power supply pin for DC/DC converter)
- VCC(Power supply pin for drive circuit)
- •GND(Ground pin for drive circuit)
- ·IN1, IN2, IN3, IN4(Control input pin)

The IN1, IN2, IN3 and IN4 pin is a pin used to determine output logic.

IN1	IN2	IN3	IN4	T1G	T2G	T3G	T4G
L	Χ	Χ	Χ	L	Χ	Χ	Χ
Н	Χ	Χ	Χ	Н	Χ	Χ	Χ
Χ	L	Χ	Χ	Χ	L	Χ	Χ
Χ	Н	Χ	Χ	Χ	Н	Χ	Χ
X	Χ		Χ	Χ	Χ	اــا	Χ
Χ	Χ	Н	Χ	Χ	Χ	Η	Χ
X	X	Χ	L	Χ	X	Χ	Ĺ
X	Χ	Χ	Н	Χ	Χ	Χ	Н

·RTC(Recovery time of protection circuit control pin)

When abnormality occurs (UVLO, short circuit detected), this pin is used to adjust the recovery time.

· ALM(Alarm signal output pin)

When abnormality occurs (UVLO, short circuit detected), This pin outputs an alarm signal. (Open drain)

Status						
While in normal operation	Hi-Z					
UVLO, When detecting short circuit	L					



#### **■**Description

1. Undervoltage Lockout (UVLO) function

The control circuit incorporates the undervoltage lockout (UVLO) function both on the VCC and the OUT(H) sides. When the OUT(H) voltage drops to the UVLO ON voltage, the Output pin and the ALM pin both will output the "L" signal.

When the VCC voltage drops to the UVLO ON voltage, the Output pin will output the "L" signal. When the VCC or the OUT(H) voltage rises to the UVLO OFF voltage, these pins will be reset.

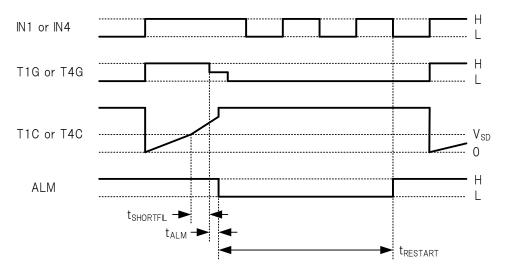
2. Short circuit protection function, Soft turn-off function (T1, T4)

When the collector pin voltage exceeds  $V_{\text{SD}}$ , the short circuit protection function will be activated.

When the short circuit protection function is activated, the OUT pin voltage will be set to the "L" level, and then the ALM pin voltage to the "L" level.

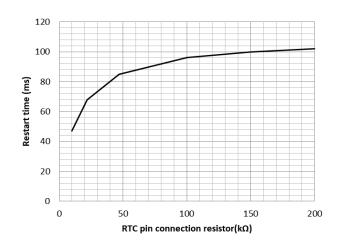
Also, soft turn-off function works to reduce collector voltage surge due to short circuit current.

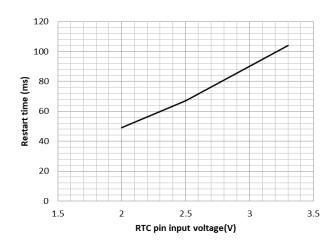
Short-circuit protection is canceled automatically after an abnormal condition restart time and when the input signal is "L" level.



Timing chart of short circuit protection function

The restart time can be adjusted within the following range by the resistance or voltage connected to the RTC pin.







#### **■**Description

3. Active clamp gate function None

# ■Reliability

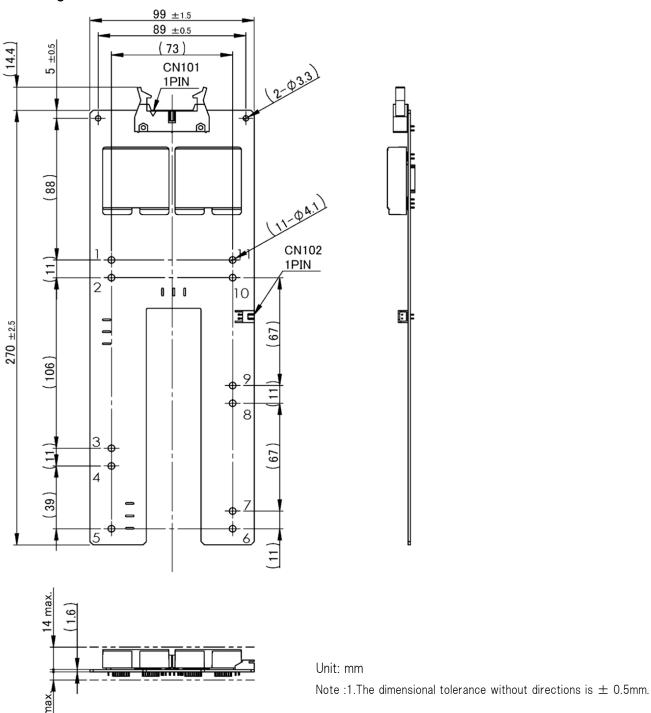
ltem	Test condition and acceptance criterion
Exposure in high temperature	IEC60068-2-2:2007: Test: Bb, 90°C, 240H, ※
Exposure in low temperature	IEC60068-2-1:2007: Test: Ab, -40°C, 240H, ※
Exposure in high temperature and high humidity	IEC60068-2-78:2012-10, 60℃, 90~95%RH, 240H, ※
Thermal shock	IEC60068-2-14:2009-01: Test: Na, -40°C/30min to 100°C/30min, 500cycles, Ж
Low temperature operation	IEC60068-2-1:2007: Test: Ae, Input voltage: DC24V, Output current: Rated Load
	-40°C, 240H, ※
High temperature operation	IEC60068-2-2:2007: Test: Be, Input voltage: DC24V, Output current: Rated Load
	85°C, 240H, ※
high temperature	IEC60068-2-78:2012-10, Input voltage: DC24V, Output current: Rated Load
and high humidity operation	85℃, 85%RH, 240H, ※
Vibration	IEC 60068-2-6:2007-12, Frequency range: 10 to 55Hz, Sweep rate: 1.0oct/min,
	Displacement amplitude: $\pm$ 0.75mm, Test duration: 120min, Axis: X,Y and Z, $$
	IEC 60068-2-6:2007-12, Frequency range: 5 to 200Hz, Sweep rate: 1.0oct/min,
	Cross-over frequency: 8.4Hz, , Displacement amplitude(5 to 8,4Hz): ±3.5mm,
	Acceleration amplitude(8.4 to 200Hz):1G, Test duration: 20sweeps, Axis: X, Y and Z, 💥
Impact	IEC 60068-2-27:2008-02, Pulse shape: Half-sine, Corresponding duration: 11ms,
	Peak acceleration: 50G, Number of shocks:3, Axis: ±X, Y and Z, 💥
	IEC 60068-2-27:2008-02, Pulse shape: Half-sine, Corresponding duration: 6ms,
	Peak acceleration: 15G, Number of shocks:100, Axis: $\pm$ X, Y and Z, $\times$

 $<sup>\</sup>ensuremath{\mathbb{X}}\xspace$  After each test, exposure at room temperature and humidity condition for 24 hours.

There shall be no abnormality on the electrical specification and appearance.



# ■Outline Dimensional Drawing



# ■Product Weight

145g(TYP)

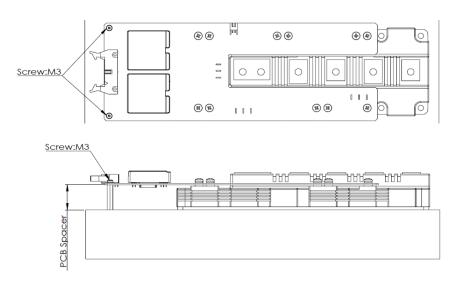
#### **■**Storage Conditions

ltem	Min	Max	Unit	Conditions · Note
Storage temperature	-25	60	$^{\circ}$ C	A packing state

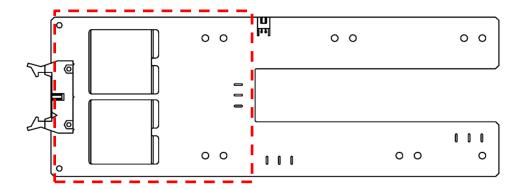


#### **■**Usage Cautions

- Always mount fuse on the plus side of input for ensuring safety because the fuse is not built-in the product. Please select the fuse considering conditions such as steady current, inrush current, and ambient temperature. When using a fuse having large rated current or high capacity input electrolytic condenser, by combining another converter and input line and input electrolytic condenser, fuse may not blow off in the case of abnormality. Do not combine high voltage line and fuse.
- Make sure the rise/fall time of the input signal is 500ns or less.
   Also, keep input wiring as far as possible from noise sources.
   To prevent malfunction due to noise, we recommend the highest possible signal voltage within the recommended range.
- Please do not apply excessive stress to this product when attaching to IGBT power module.
  Please follow the device manufacturer's instructions on how to install the IGBT power module
  (type of screw used, material, tightening torque conditions, etc.).
  Also, if the product is exposed to vibration or shock, the PCB should be fixed with spacers as shown in the figure below.



Handling of this product before IGBT installation In order to avoid damage to mounted components due to warpage of the PC board, the product should be gripped within the area indicated by the red dotted line in the figure below.



■ This product has DESAT protection for arm short circuit and load short circuit protection.
However, even if this protection works, the IGBT may be damaged if abnormally high current occurs due to IGBT's characteristics variations or the load short-circuit mode during parallel operation.

To ensure safety, be sure to check the short-circuit current at the unit in which this product is integrated, and evaluate whether it can protect under the condition that there is no damage to the IGBT.



#### ■Important Notice

- This information and product are subject to change without prior notice for the purpose of improvements, etc. Ensure that you are in possession of the most up-to-date information when using this product.
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  - Use in liquids such as water, oil, chemical solutions, or organic solvents, and use in locations where the product will be exposed to such liquids.
  - · Use that involves exposure to direct sunlight, outdoor exposure, or dusty conditions.
  - · Use in locations where corrosive gases such as salt air, C12, H2S, NH3, S02, or NO2, are present.
  - $\boldsymbol{\cdot}$  Use in environments with strong static electricity or electromagnetic radiation.
  - $\cdot$   $\,$  Use that involves placing inflammable material next to the product.
  - · Use of this product either sealed with a resin filling or coated with resin.
  - · Use of water or a water soluble detergent for flux cleaning.
  - · Use in locations where condensation is liable to occur.
- This product is not designed to resist radiation.
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